

Figure 7-1. Timing Generator Module M712, Schematic Diagram, M712-0-1

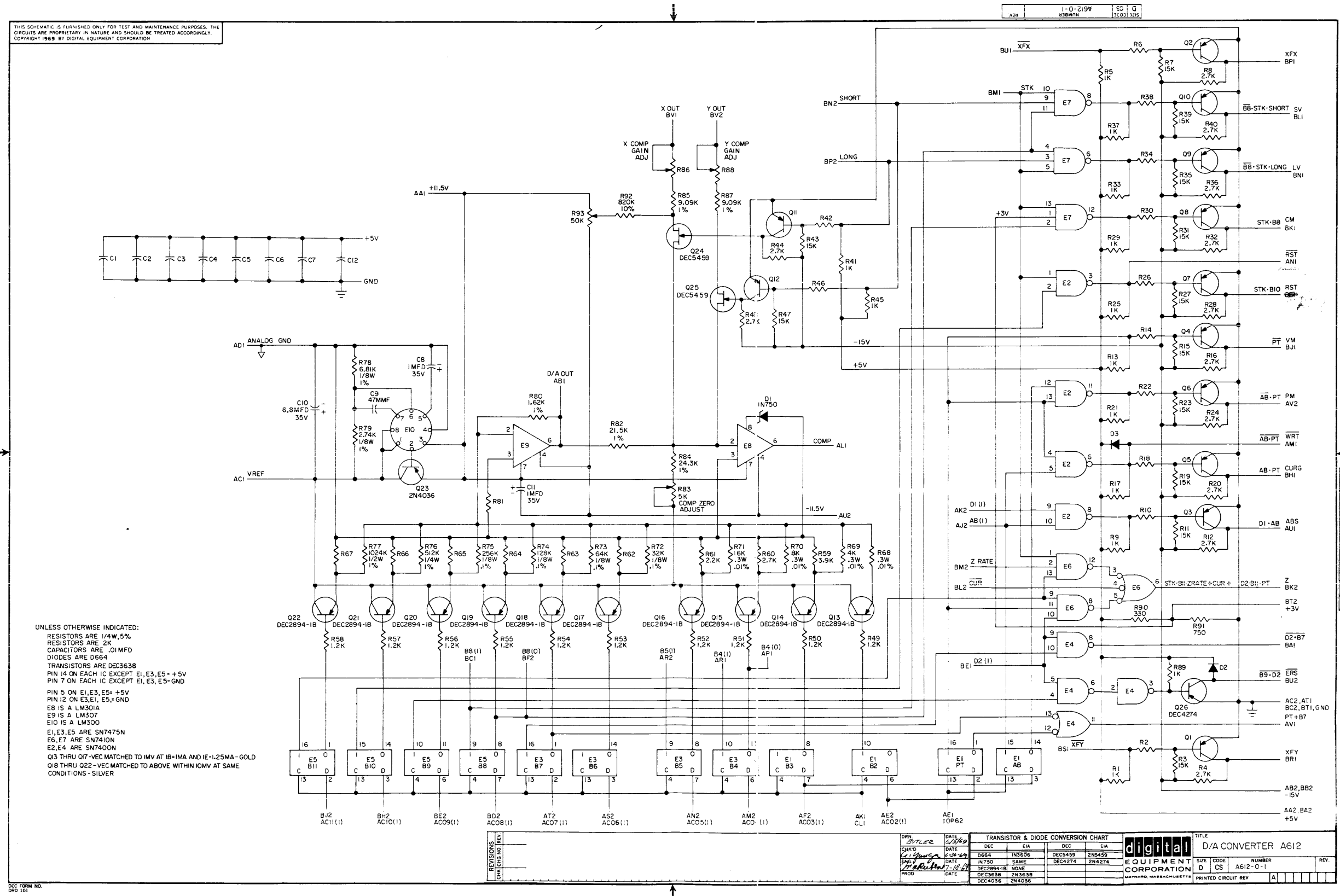
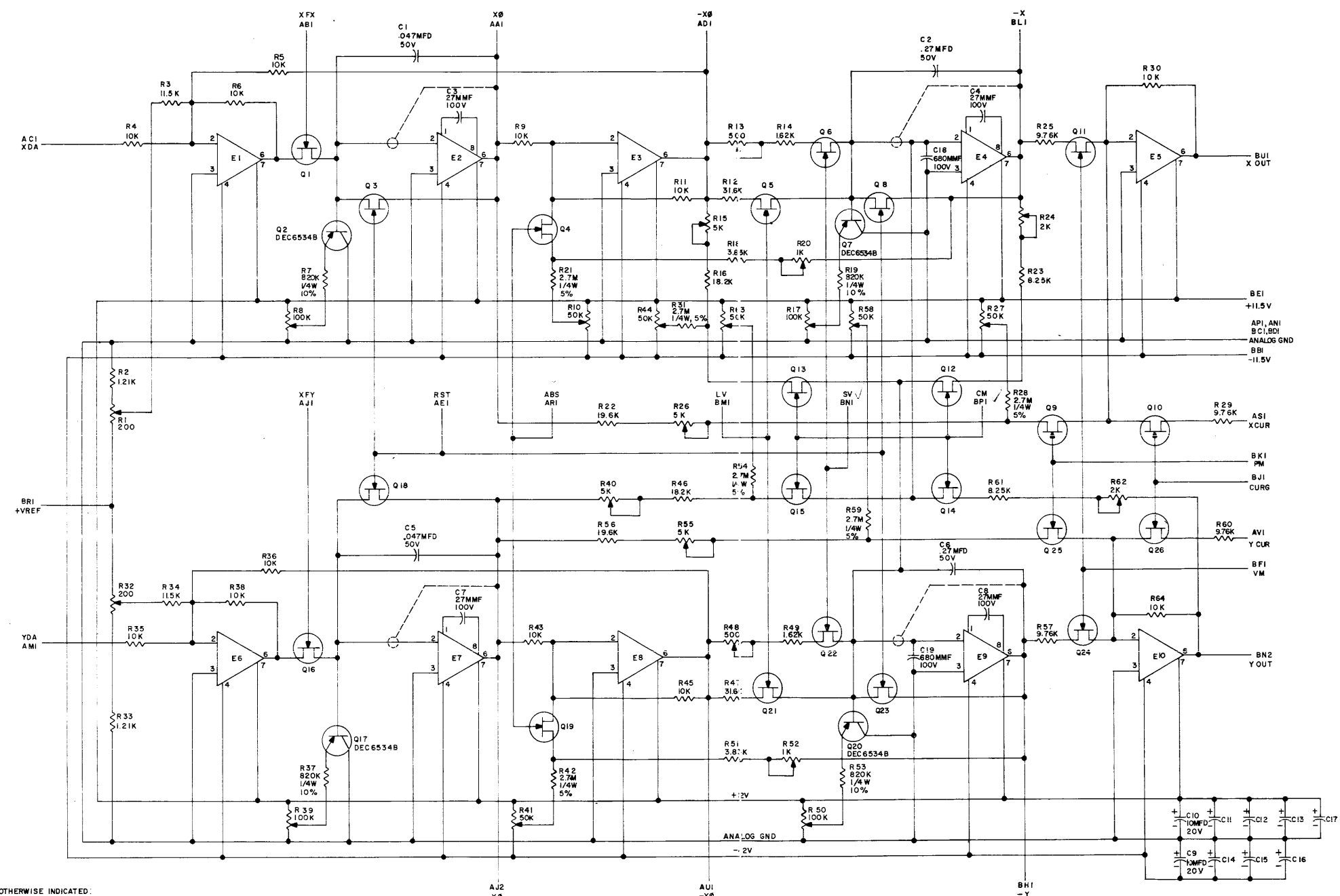


Figure 7-2. Digital-to-Analog Converter and Gate Logic Module A612, Schematic Diagram, A612-0-1

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SIZE CODE NUMBER  
D CS A312-0-1



UNLESS OTHERWISE INDICATED:  
CAPACITORS ARE 1MFD 35V  
E1-E9 ARE LM307  
TRANSISTORS ARE 2N5459 FET  
RESISTORS ARE 1%, 1/8W  
R16, R17, R32, R39, R50 ARE HELITRIM POT TYPE 78 PR  
ALL OTHER POTS ARE HELITRIM TYPE 62 PR

REV. NO.	DATE	TRANSISTOR & DIODE CONVERSION CHART		TITLE
		DEC	EIA	
1	7/1/69	2N5459	NONE	ANALOG FUNCTION GENERATOR A312
2	7/1/69	DEC 6534B	MP56534	

REVISIONS	DATE	BY	CHKD
1	7/1/69	W. J. ...	W. J. ...
2	7/1/69	W. J. ...	W. J. ...

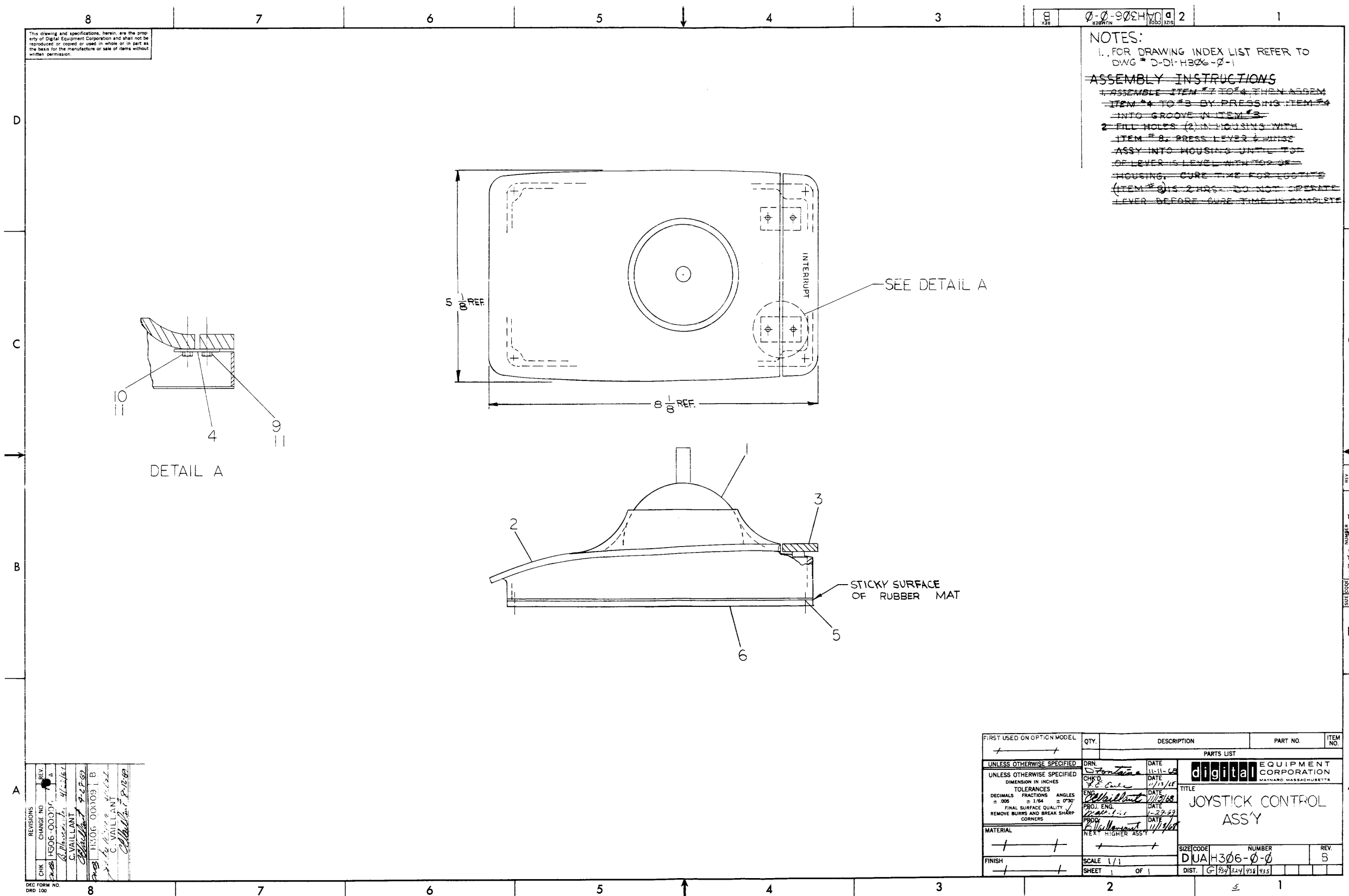
DIGITAL EQUIPMENT CORPORATION	SIZE CODE NUMBER	REV.
1	D CS A312-0-1	E

DEC FORM NO. 010 101

Figure 7-3. Analog Function Generator Module A312, Schematic Diagram, A312-0-1







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NOTES:  
 1. FOR DRAWING INDEX LIST REFER TO DWG # D-DI-H306-0-1  
~~ASSEMBLY INSTRUCTIONS~~  
~~1. ASSEMBLE ITEM #2 TO #4, THEN ASSEMBLE ITEM #4 TO #3 BY PRESSING ITEM #4 INTO GROOVE IN ITEM #3~~  
~~2. FILL HOLES (2) IN HOUSING WITH ITEM #8. PRESS LEVER & MISC ASSY INTO HOUSING UNTIL TOP OF LEVER IS LEVEL WITH TOP OF HOUSING. CURE TIME FOR EPOXY (ITEM #8) IS 2 HRS. DO NOT OPERATE LEVER BEFORE CURE TIME IS COMPLETE~~

REV.	CHANGE NO.	DATE
A		

DEC FORM NO. ORD 100

FIRST USED ON OPTION MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST					
UNLESS OTHERWISE SPECIFIED		DRN.	DATE	<b>digital</b> EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
UNLESS OTHERWISE SPECIFIED		CHK'D.	DATE		
TOLERANCES		ENG.	DATE		
DIMENSION IN INCHES		PROJ. ENG.	DATE		
DECIMALS		PROJ.	DATE	TITLE <b>JOYSTICK CONTROL ASSY</b>	
FRACTIONS					
ANGLES				SIZE CODE NUMBER <b>DUAH306-0-0</b>	
FINAL SURFACE QUALITY					
REMOVE BURRS AND BREAK SHARP CORNERS				REV. <b>5</b>	
MATERIAL					
FINISH		SCALE	1/1	DIST. G-102/124/938/935	
SHEET		1	OF 1		

Figure 7-12. Joystick Control Assembly, H306-0-0